

KESD06035V

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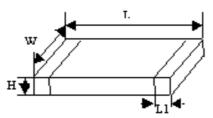
	01.1	X 7.1	TL.5/				
1.1 Technical Data	Symbol	Value	Unit				
Maximum allowable continuous DC voltage	V _{DC}	5.0	V				
Varistor voltage	V_B	35.0	V				
Varistor voltage tolerance		± 20	%				
Typical capacitance value measured*3	С	5	pF				
Typical capacitance value tolerance		4.5	pF				
Maximum clamping voltage measured*4	$V_{\rm C}$	60	V				
1.2 Reference Data							
Response time	T _{rise}	<1	ns				
Leakage current at Varistor voltage	I _{LVB}	<10	μA				
Operating ambient temperature		-40~+85	°C				
Storage temperature		-40~+125	°C				
Reflow temperature profile(Recommend)		260	°C				
1.3 Other Data							
Body	ZnO						
End termination	Ag/Pd Ag/	Ni/Sn					
Packaging	Bulk/Tape						
Complies with Standard	IEC61000-4-2、IEC61000-4-5						
Notes:							
*1 AC voltage at 50~60Hz							
*2 Varistor voltage	Measured at 1mA	DC					
*3 Capacitance	Measured at f=1k	,	1.4				
*4 Maximum clamping voltage	Measured at 30ns	by IEC6100-4-2,30A@8kV,leve	814				

Size:

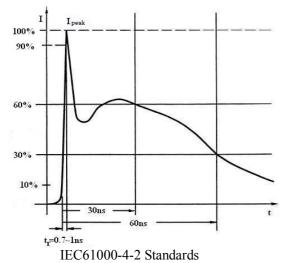
Size:				Unit: mm	اسل
Туре	Length (L)	Width (W)	High (H)	Termination (L1)	w /
0603=1608	1.60±0.20	0.80±0.20	0.80±0.20	0.30 ± 0.20	
					н ф

2.Size

Model	1005(0402)	1608(0603)	2012(0805)	3216(1206)	3225(1210)	4532(1812)	5650(2220)	8050(3220)
Length(L)	1.00 ± 0.15	1.60 ± 0.20	2.00 ± 0.20	3.20 ± 0.20	3.20 ± 0.20	4.50 ± 0.20	5.60 ± 0.20	8.0 ± 0.30
Width(W)	0.50 ± 0.15	0.80 ± 0.20	1.20 ± 0.20	1.60 ± 0.20	2.50 ± 0.20	3.20 ± 0.20	5.00 ± 0.20	5.00 ± 0.30
High(H)	0.70max	0.90max	1.30max	1.60max	2.50max	3.20max	4.50max	4.50max



3.ESD Wave Form



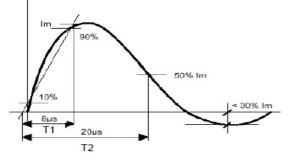
SEVERITY LEVEL	AIRDIRCHARGE	DIRECT ISCHARGE
1	2 kV	2 kV
2	4 kV	4 kV
3	8 kV	6 kV
4	15 kV	8 kV

IEC61000-4-2 Compliant ESD Current Pulse Waveform



4.Surge Wave Form

Wave shape "Short circuit" (Current Isc)



SEVERITY LEVEL	T1	T2
1	8 μs	20 µs

IEC61000-4-5 Standards

5.Enviromental Reliability Test

Characteristic	Test method and description									
High Temperature Storage	The specimen shall be subjected to 125° C for 1000 hours in a thermostatic bath wither load and then stored at room temperature and humidity for 1 to 2 hours. The change varistor voltage shall be within 10%.									
	The temperature cycle of specified	Step	Temperature	Period						
	temperature shall be repeated five times and	1	-40±3℃	$30 \text{min} \pm 3$						
Temperature Cycle	then stored at room temperature and humidity for one two hours. The change of	2	Room Temperature	1~2hours						
	varistor voltage shall be within 10% and	3	125±2℃	$30 \text{min} \pm 3$						
	mechanical damage shall be examined.	4	Room Temperature	1~2hours						
High Temperature Load	After being continuously applied the maximum allowable voltage at 85°C for 1000hours, the specimen shall be stored at room temperature and humidity for one or hours, the change of varistor voltage shall be within 10%.									
Damp Heat Load/ Humidity Load	The specimen should be subjected to 40° C, 90 to 95%RH environment, and the maximum allowable voltage applied for 1000 hours, then stored at room temperature and humidity for one or two hours. The change of varistor voltage shall be within 10%.									
Low Temperature Storage	The specimen should be subjected to -40° , without load for 1000 hours and then stored at room temperature for one two hours. The change of varistor voltage shall be within 10%.									

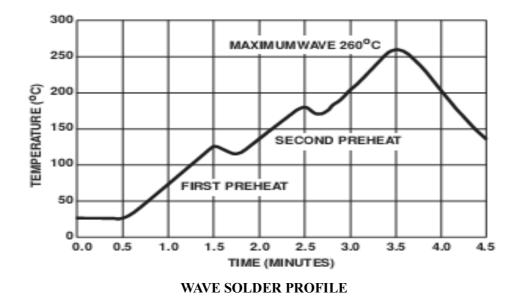
6.Soldering Recommendation

The principal techniques used for the soldering of components in surface mount technology are infrared reflow and wave soldering.

6.1 Wave Soldering

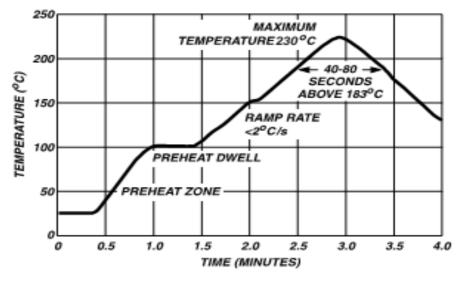
When wave soldering. The MLCV is attach to the circuit board by means of an adhesive. The assembly is then place on a conveyor and run though the soldering process to contact the wave. Wave soldering is the most strenuous of the processes. To avoid the possibility of generating stresses due to thermal shock., a preheat stage in the soldering process is recommended, and the peak temperature of the solder process should be rigidly controlled. The following is the typical profiles.





6.2 Reflow Soldering

When reflow soldering, the device is placed a solder paste on the substrate ,as the solder paste is heated, it re-flows and solders the unite to board. When using a reflow process ,care should be taken to ensure that the MLCV is not subjected to an thermal gradient steeper than 4 degrees per second; the ideal gradient being 2degrees per second. During the soldering process, preheating to within 100 degrees of the soldier's peak temperature is essential to minimize thermal shock. The following is typical profile.





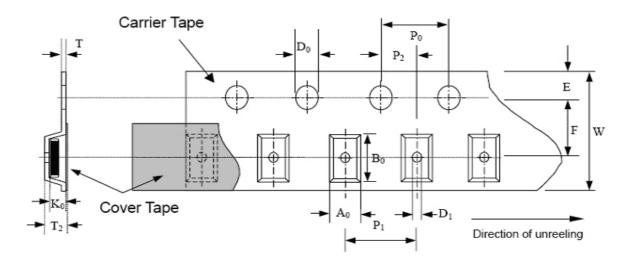


7 Packaging Specification

7.1 Carrier tape transparent cover tape should be heat-sealed to carry the products, and the reel should be used to reel the carrier tape.

7.2 The adhesion of the heat-sealed cover tape shall be 40 + 20/ - 15 grams.

7.3 Both the head and the end portion of taping shall be empty for reel package and SMT auto-pickup machine. And a normal paper tape shall be connected in the head of taping for the operator handle.



type	A ₀ ±0.10	B_0 ±0.10	K ₀ ±0.10	T ±0.05	$\begin{array}{c} T_2 \\ \pm 0.05 \end{array}$	D ₀ +0.10 -0.00	D ₁ ±0.05	P ₁ ±0.10	P ₂ ±0.05	P ₀ ±0.05	W ±0.20	Е ±0.10	F ±0.05
1005	1.08	1.88	1.04	0.22	0.10	1.50	1.00	4.00	2.00	4.00	8.00	1.75	3.50
1608	1.08	1.88	1.04	0.22	0.10	1.50	1.00	4.00	2.00	4.00	8.00	1.75	3.50
2012	1.42	2.30	1.04	0.22	0.10	1.50	1.00	4.00	2.00	4.00	8.00	1.75	3.50
3216	1.88	3.50	1.27	0.20	0.10	1.50	1.00	4.00	2.00	4.00	8.00	1.75	3.50
3225	2.18	3.46	1.45	0.22	0.10	1.50	1.00	4.00	2.00	4.00	8.00	1.75	3.50
4532	3.66	4.95	1.74	0.25	0.10	1.50	1.50	8.00	2.00	4.00	12.00	1.75	5.50
5650	5.10	5.97	2.80	0.25	0.10	1.50	1.50	8.00	2.00	4.00	12.00	1.75	5.50